## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ın re Appı	ication of:	)			
	Chen et al.	) ) )	Examiner:	Faherty, Corey S.	
Serial No.	: 10/789,732	)	Art Unit:	2183	
Filed:	February 27, 2004	)	Confirmation	n No.:	8404
SY IN	PROCESSOR SYNCHRONIZATION IN A MULTI-PROCESSOR COMPUTER SYSTEM				

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## SUPPLEMENTAL AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Office Action mailed on February 1, 2008, Applicants respectfully request the Examiner to enter the following amendments and consider the following remarks for allowance of the above-identified patent application.

200400184-1 - 1 - Serial No.: 10/789,732 Examiner: Faherty, Corey S. Group Art Unit: 2183